

ABSTRACT

Memory devices and methods that provide a nonvolatile buffered memory interface are disclosed. In one embodiment, a memory device may comprise: a nonvolatile memory array, and a nonvolatile buffered memory interface integrated on a substrate with the memory array. The memory interface may comprise one or more volatile buffers configured to buffer data for read operations, and a table memory configured to indicate one or more addresses associated with data buffered in the one or more volatile buffers.